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APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.
10/698,329	10/31/2003	Donald A. Ice	15436.122.1	9808
22913	7590	10/19/2005	EXAMINER	
WORKMAN NYDEGGER (F/K/A WORKMAN NYDEGGER & SEELEY) 60 EAST SOUTH TEMPLE 1000 EAGLE GATE TOWER SALT LAKE CITY, UT 84111			LEVI, DAMEON E	
			ART UNIT	PAPER NUMBER
			2841	

DATE MAILED: 10/19/2005

Please find below and/or attached an Office communication concerning this application or proceeding.

Office Action Summary

Application No.

10/698,329

Applicant(s)

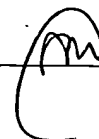
ICE, DONALD A.

Examiner

Dameon E. Levi

Art Unit

2841



-- The MAILING DATE of this communication appears on the cover sheet with the correspondence address --

Period for Reply

A SHORTENED STATUTORY PERIOD FOR REPLY IS SET TO EXPIRE _ MONTH(S) OR THIRTY (30) DAYS, WHICHEVER IS LONGER, FROM THE MAILING DATE OF THIS COMMUNICATION.

- Extensions of time may be available under the provisions of 37 CFR 1.136(a). In no event, however, may a reply be timely filed after SIX (6) MONTHS from the mailing date of this communication.
- If NO period for reply is specified above, the maximum statutory period will apply and will expire SIX (6) MONTHS from the mailing date of this communication.
- Failure to reply within the set or extended period for reply will, by statute, cause the application to become ABANDONED (35 U.S.C. § 133). Any reply received by the Office later than three months after the mailing date of this communication, even if timely filed, may reduce any earned patent term adjustment. See 37 CFR 1.704(b).

Status

- 1) ☐ Responsive to communication(s) filed on ____.
- 2a) ☒ This action is **FINAL**. 2b) ☐ This action is non-final.
- 3) ☐ Since this application is in condition for allowance except for formal matters, prosecution as to the merits is closed in accordance with the practice under *Ex parte Quayle*, 1935 C.D. 11, 453 O.G. 213.

Disposition of Claims

- 4) ☒ Claim(s) 21-40 is/are pending in the application.
- 4a) Of the above claim(s) ____ is/are withdrawn from consideration.
- 5) ☐ Claim(s) ____ is/are allowed.
- 6) ☒ Claim(s) 21-40 is/are rejected.
- 7) ☐ Claim(s) ____ is/are objected to.
- 8) ☐ Claim(s) ____ are subject to restriction and/or election requirement.

Application Papers

- 9) ☐ The specification is objected to by the Examiner.
- 10) ☐ The drawing(s) filed on ____ is/are: a) ☐ accepted or b) ☐ objected to by the Examiner.
Applicant may not request that any objection to the drawing(s) be held in abeyance. See 37 CFR 1.85(a).
Replacement drawing sheet(s) including the correction is required if the drawing(s) is objected to. See 37 CFR 1.121(d).
- 11) ☐ The oath or declaration is objected to by the Examiner. Note the attached Office Action or form PTO-152.

Priority under 35 U.S.C. § 119

- 12) ☐ Acknowledgment is made of a claim for foreign priority under 35 U.S.C. § 119(a)-(d) or (f).
- a) ☐ All b) ☐ Some * c) ☐ None of:
- ☐ Certified copies of the priority documents have been received.
 - ☐ Certified copies of the priority documents have been received in Application No. ____.
 - ☐ Copies of the certified copies of the priority documents have been received in this National Stage application from the International Bureau (PCT Rule 17.2(a)).
- * See the attached detailed Office action for a list of the certified copies not received.

Attachment(s)

- | | |
|---|--|
| 1) <input type="checkbox"/> Notice of References Cited (PTO-892) | 4) <input type="checkbox"/> Interview Summary (PTO-413)
Paper No(s)/Mail Date. ____ |
| 2) <input type="checkbox"/> Notice of Draftsperson's Patent Drawing Review (PTO-948) | 5) <input type="checkbox"/> Notice of Informal Patent Application (PTO-152) |
| 3) <input type="checkbox"/> Information Disclosure Statement(s) (PTO-1449 or PTO/SB/08)
Paper No(s)/Mail Date ____ | 6) <input type="checkbox"/> Other: ____ |

DETAILED ACTION

Claim Rejections - 35 USC § 102

The following is a quotation of the appropriate paragraphs of 35 U.S.C. 102 that form the basis for the rejections under this section made in this Office action:

A person shall be entitled to a patent unless –

(b) the invention was patented or described in a printed publication in this or a foreign country or in public use or on sale in this country, more than one year prior to the date of application for patent in the United States.

Claims 21-40 are rejected under 35 U.S.C. 102(b) as being anticipated by Kim et al US Patent 5666271.

Regarding claim 21, Kim et al discloses an assembly comprising:

a chassis, comprising a housing(for example, see elements 4,6, Figs 2-4);

a backplane (for example, see elements 2, Figs 2-4)attached to the housing; and

a cover (for example, see elements 3,5,5a,2 Figs 2-4)attached to the backplane and to the housing;

a printed circuit board assembly substantially disposed within the chassis(for example, see column 4, lines 25-27, and 37-41,see Figs 2-4); and

a plurality of card guides (for example, see elements 1, Figs 2-4)substantially disposed within the chassis in a spaced apart arrangement with respect to each other, each of the card guides being engaged with the printed circuit board assembly, the backplane and the cover(for example, see Figs 2-4).

Regarding claim 22, Kim et al discloses wherein each of the card

guides comprises: a first engagement element (for example, see card guide slots or channels on elements 1, Figs 2-4)that engages the printed circuit board assembly; a

Art Unit: 2841

second engagement element(for example, see elements 10, Figs 2-4) that engages the backplane; and a third engagement element (for example, see elements 11a, Figs 2-4)that engages the cover.

Regarding claim 23, Kim et al discloses wherein the cover includes a self-clinching tie mount arranged to engage the third engagement element(for example, see elements 7d, Figs 2-4).

Regarding claim 24, Kim et al discloses wherein adjacent card guides are disposed opposite each other at a distance generally corresponding to a width of a functional module card(for example, see elements 1, Figs 2-4).

Regarding claim 25, Kim et al discloses wherein each card guide is releasably engaged with the printed circuit board assembly, the backplane and the cover(for example, see elements 1, Figs 2-4).

Regarding claim 26, Kim et al discloses wherein the plurality of card guides collectively define a plurality of card storage levels(for example, see elements 1, Figs 2-4).

Regarding claim 27, Kim et al discloses wherein the plurality of card guides comprises first and second end card guides and a middle card guide, the first and second card guides being positioned on either side of the middle card guide such that: a distance between the middle card guide and the first end card guide generally corresponds to a width of a functional module card; and a distance between the middle card guide and the second end card guide generally corresponds to a width of a functional module card(for example, see arrangements and spacing of the three elements 1, Figs 2-4).

Art Unit: 2841

Regarding claim 28, Kim et al discloses, wherein the plurality of card guides collectively define at least two side-by-side card storage slots(for example, see arrangements of the elements 1, Figs 2-4).

Regarding claim 29, Kim et al discloses, wherein the plurality of card guides collectively define at least two stacked card storage slots(for example, see arrangements of the elements 1, Figs 2-4).

Regarding claim 30, Kim et al discloses wherein the plurality of card guides collectively define: at least two stacked card storage slots; and at least two side-by-side card storage slots(for example, see arrangements of the elements 1, Figs 2-4).

Regarding claim 31, Kim et al discloses an assembly comprising:

a plurality of engagement elements, where:

a first engagement element of the plurality comprises a portion of a printed circuit board assembly(for example, see column 4, lines 25-27, and 37-41)

a second engagement element of the plurality comprises a portion of a backplane of an electronic equipment chassis(for example, see elements 7c, Figs 2-4); and

a third engagement element of the plurality comprises a portion of a cover of an electronic equipment chassis(for example, see elements 7d, Figs 2-4); and

corresponding structure configured to engage the plurality of engagement elements, the corresponding structure comprising a portion of one or more card guides(for example, see elements 1,10,11a, and guide slots on elements 1, Figs 2-4).

Art Unit: 2841

Regarding claim 32, Kim et al discloses wherein a portion of the corresponding structure is configured to releasably engage an engagement element(for example, see elements 11a, Figs 2-4).

Regarding claim 33, Kim et al discloses wherein a portion of the corresponding structure is configured to permanently engage an engagement element(for example, see elements 10, Figs 2-4).

Regarding claim 34, Kim et al discloses an assembly comprising:

a chassis, comprising a housing(for example, see elements 4,6, Figs 2-4);

a backplane attached to the housing(for example, see elements 2 Figs 2-4); and

a cover attached to the backplane and to the housing(for example, see elements 3, Figs 2-4);

a plurality of self clinching tie mounts attached to the cover(for example, see elements 7d Figs 2-4);

a printed circuit board assembly substantially disposed within the interior of the electronic equipment chassis(for example, see column 4, lines 25-27, and 37-41); and

a plurality of card guides(for example, see elements 1, Figs 2-4) disposed within the chassis in a spaced apart arrangement with respect to each other, each of the card guides engaged with a corresponding self-clinching tie mount(for example, see elements 3,11a,7d Figs 2-4).

Regarding claim 35, Kim et al discloses wherein adjacent card guides are disposed opposite each other at a distance generally corresponding to a width of a functional module card(for example, see elements 1, Figs 2-4).

Art Unit: 2841

Regarding claim 36, Kim et al discloses wherein the plurality of card guides collectively define a plurality of card storage levels(for example, see arrangement of elements 1, Figs 2-4).

Regarding claim 37, Kim et al discloses wherein the plurality of card guides comprises first and second end card guides and a middle card guide, the first and second card guides being positioned on either side of the middle card guide such that: a distance between the middle card guide and the first end card guide generally corresponds to a width of a functional module card; and a distance between the middle card guide and the second end card guide generally corresponds to a width of a functional module card(for example, see arrangements and spacing of the three elements 1, Figs 2-4).

Regarding claim 38, Kim et al discloses wherein the plurality of card guides collectively define at least two side-by-side card storage slots(for example, see arrangements of the elements 1, Figs 2-4).

Regarding claim 39, Kim et al discloses wherein the plurality of card guides collectively define at least two stacked card storage slots(for example, see arrangements of the elements 1, Figs 2-4).

Regarding claim 40, Kim et al discloses wherein the plurality of card guides collectively define: at least two stacked card storage slots; and at least two side-by-side card storage slots(for example, see arrangements of the elements 1, Figs 2-4).

Response to Arguments

Applicant's arguments fail to comply with 37 CFR 1.111(b) because they amount to a general allegation that the claims define a patentable invention without specifically pointing out how the language of the claims patentably distinguishes them from the references.

Applicant's amendment necessitated the new ground(s) of rejection presented in this Office action. Accordingly, **THIS ACTION IS MADE FINAL**. See MPEP § 706.07(a). Applicant is reminded of the extension of time policy as set forth in 37 CFR 1.136(a).

A shortened statutory period for reply to this final action is set to expire THREE MONTHS from the mailing date of this action. In the event a first reply is filed within TWO MONTHS of the mailing date of this final action and the advisory action is not mailed until after the end of the THREE-MONTH shortened statutory period, then the shortened statutory period will expire on the date the advisory action is mailed, and any extension fee pursuant to 37 CFR 1.136(a) will be calculated from the mailing date of the advisory action. In no event, however, will the statutory period for reply expire later than SIX MONTHS from the date of this final action.

Any inquiry concerning this communication or earlier communications from the examiner should be directed to Dameon E. Levi whose telephone number is (571) 272-2105. The examiner can normally be reached on Mon.-Fri. (9:00 - 5:00).

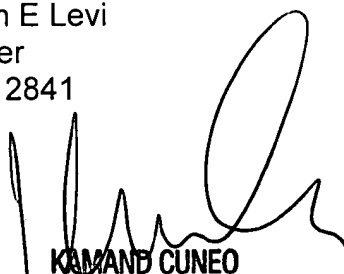
Art Unit: 2841

If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, Kamand Cuneo can be reached on (571) 272-1957. The fax phone number for the organization where this application or proceeding is assigned is 571-273-8300.

Information regarding the status of an application may be obtained from the Patent Application Information Retrieval (PAIR) system. Status information for published applications may be obtained from either Private PAIR or Public PAIR. Status information for unpublished applications is available through Private PAIR only. For more information about the PAIR system, see <http://pair-direct.uspto.gov>. Should you have questions on access to the Private PAIR system, contact the Electronic Business Center (EBC) at 866-217-9197 (toll-free).

DEL

Dameon E Levi
Examiner
Art Unit 2841



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SUPERVISORY PATENT EXAMINER
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